

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|---|------------------|---------|------------------|
| L1 | 8 | ((level adj1 shift\$3) or (level adj1 convert\$3)) and (cascode NEAR1 transistor\$1) and (transistor\$1 NEAR1 seri\$4) and (keeper or latch\$2 or storage) and (bias\$3 or reference) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/22 14:11 |
| L2 | 566 | ((level adj1 shift\$3) or (level adj1 convert\$3)) and (transistor\$1 NEAR1 seri\$4) and (keeper or latch\$2 or storage) and (bias\$3 or reference) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/22 14:20 |
| L3 | 306 | 2 and ((complemen\$4 NEAR1 output\$1) or (inver\$4 NEAR1 output\$1)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/22 14:21 |
| L4 | 48 | 3 and (326/62-83.ccls. or 327/333.ccls.) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/12/22 14:23 |

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|----------|------------------|---------|------------------|
| L1 | 30 | ((cascode NEAR2 transistor\$1) or (seri\$4 NEAR2 transistor\$1)) and (bias or reference) and ((complement\$4 NEAR1 output\$1) or (inver\$4 NEAR1 output)) and (keeper\$1 or latch\$2 or storage)). clm. | US-PGPUB | OR | OFF | 2005/12/22 14:43 |